



AI for Design and Verification Seminar

For RTL Design & Verification Engineers

Artificial Intelligence is rapidly transforming how complex hardware systems are designed and verified. This seminar gives RTL engineers a practical understanding of modern AI tools and explores how they can be applied to real-world design and verification challenges.

Seminar Goals

- Learn the core AI toolsets and how they work:
 - Machine Learning
 - Neural Networks
 - Large Language Models (LLMs)
 - AI Agents
- Review real-world examples of AI used in design and verification
- Explore where AI can provide the greatest leverage in RTL workflows

Seminar Structure

8 Sessions	Once a week, 2 hours per session
1 Introductory Session	Overview of AI concepts and relevance to RTL Design & Verification
7 Discussion Sessions	Presentation and discussion of recent AI-related articles and practical applications

Participant Expectations

- Read and present 1–2 recent articles
- Listen to curated podcasts ahead of sessions (~30 minutes per week)
- Participate in technical discussions and brainstorming around practical AI usage

For More Information

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